


**MICRO-FUSE AND ITS MANUFACTURING METHOD**

**Patent number:** JP2002042632  
**Publication date:** 2002-02-08  
**Inventor:** TAMURA AKIHIKO  
**Applicant:** MATSUO ELECTRIC CO LTD  
**Classification:**  
- international: H01H85/00; H01H69/02  
- european:  
**Application number:** JP20000223536 20000725  
**Priority number(s):**

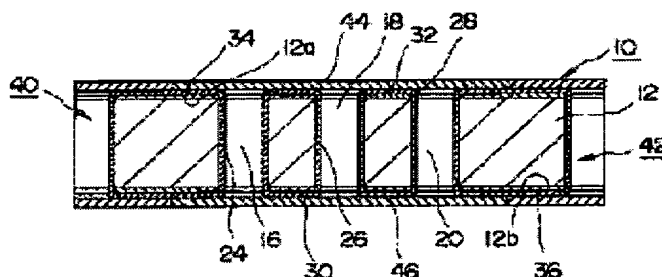
Also published as:

 JP2002042632 (A)

**Abstract of JP2002042632**

**PROBLEM TO BE SOLVED:** To provide a micro-fuse with high pulse resistance characteristics.

**SOLUTION:** Fuse layers 24, 26, 28 are formed on the inner surfaces on plural holes 16, 18, 20 installed so as to pass through from the surface 12a to the surface 12b of an insulator 12, and these fuse layers 24, 26, 28 are connected with connecting conductors 30, 32, 36. At least one of shapes, diameters, and distances between the holes of the holes 16, 18, 20 is varied to obtain the desirable fusing characteristics.



Data supplied from the **esp@cenet** database - Worldwide